

IN THE CLAIMS:

The status of the claims is noted below.

Claims 1-52 (Cancelled).

53. (Original) An editor for modifying a processing sequence by editing the configuration of a processing web, comprising:

means for determining a current state of said processing web, including one or more processing elements and connections therebetween in said processing web; and

a selector for selecting and editing at least one of said processing elements of said processing web;

whereby said processing sequence is modified in accordance with the editing of said at least one processing element.

54. (Original) The editor of claim 53, wherein said editor edits said first processing element to indicate an update time indicating a time during which said first processing element is to consume additional input data.

55. (Original) The editor of claim 54, wherein said update is controlled by an update processing element.

56. (Original) The editor of claim 53, wherein said editing of said at least one processing element includes changing a connection of at least one pin of said at least one processing element.

57. (Original) The editor of claim 53, wherein said editing of said at least one processing element includes adding another processing element to said processing web.

58. (Original) The editor of claim 57, wherein said another processing element is added to said processing web by dragging a representation of said processing element onto a display

representative of said processing web, and connecting inputs and outputs of said another processing element to the inputs and outputs of other existing processing elements.

59. (Original) The editor of claim 53, wherein said editing of said at least one processing element includes modifying the definition thereof.

60. (Original) The editor of claim 59, wherein modifying the definition of said at least one processing element includes modifying one or more operating parameters thereof.

61. (Original) The editor of claim 53, further comprising a viewing element being added to said processing web to view a live, real time output at the location of said viewing element.

62. (Original) A processing web editor for modifying a processing sequence by editing a graphical representation of a processing, comprising:

means for determining a current state of said processing web;

a renderer for generating a graphical representation of said processing web by:

determining a first processing element of said processing web;

placing said first processing element in a particular location based at least in part upon its location in said processing web and various inputs to and outputs from said first processing element;

determining a second processing element of said processing web;

placing said second processing element in a particular location based at least in part upon its location in said processing web, various inputs to and outputs from said second processing element, and a relationship between said second processing element and said first processing element; and

connecting at least one pin of said first processing element to one pin of

said second processing element; and
a selector for selecting and editing at least one processing element of said processing web;
whereby said processing sequence is modified in accordance with the editing of said at
least one processing element.

63. (Original) The editor of claim 62, wherein a list of available processing elements is provided.

64. (Original) The editor of claim 63, wherein these processing elements are categorized by function.

65. (Original) The editor of claim 62, wherein upon selection of a processing element allows for the modifying of any parameter regarding said selected processing element

66. (Original) An editor for modifying a processing sequence by editing the configuration of a processing web, comprising:

means for determining a current state of said processing web, including one or more processing elements and connections therebetween in said processing web; and

a selector for selecting and editing at least one connection between said processing elements of said processing web;

whereby said processing sequence is modified in accordance with the editing of said at least one connection.

67. (Original) The editor of claim 66, wherein when said at least one connection is requested to be edited, a determination is made whether said processing elements to be connected by said at least one connection are compatible.

68. (Original) The editor of claim 67, wherein if it is determined that said processing

elements are compatible and of the same format, the connection is made.

69. (Original) The editor of claim 67, wherein if it is determined that said processing elements are compatible but of different formats, an adapter is automatically inserted between the processing elements.

70. (Original) The editor of claim 69, wherein said adapter comprises a plurality of processing elements.

71. (Original) The editor of claim 67, wherein if it is determined that said processing elements are not compatible, a connection is not made therebetween.

72. (Original) A method for modifying a processing sequence by editing the configuration of a processing web, comprising the steps of:

determining a current state of said processing web, including one or more processing elements and connections therebetween in said processing web; and

selecting and editing at least one connection between said processing elements of said processing web;

whereby said processing sequence is modified in accordance with the editing of said at least one connection.

73. (Original) The method of claim 72, wherein when said at least one connection is requested to be edited, determining whether said processing elements to be connected by said at least one connection are compatible.

74. (Original) The method of claim 73, wherein if it is determined that said processing elements are compatible and of the same format, the connection is made.

75. (Original) The method of claim 73, wherein if it is determined that said processing

elements are compatible but of different formats, an adapter is automatically inserted between the processing elements.

76. (Original) The method of claim 75, wherein said adapter comprises a plurality of processing elements.

77. (Original) The method of claim 73, wherein if it is determined that said processing elements are not compatible, a connection is not made therebetween.